

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L22</u>	L21 and context\$1	15	<u>L22</u>
<u>L21</u>	L20 and thread\$3	18	<u>L21</u>
<u>L20</u>	sparc and register\$1 near4 window\$3 near50 (index\$6 or absolut\$4 or virtual\$4 or linear or physical\$4 or real or pag\$4 or base or offset\$)	55	<u>L20</u>
<u>L19</u>	L18 and (index\$6 or absolut\$4 or virtual\$4 or linear or physical\$4 or real or pag\$4 or base or offset\$)	162	<u>L19</u>
<u>L18</u>	L17 not l3	168	<u>L18</u>
<u>L17</u>	sparc and register\$1 near4 window\$3	169	<u>L17</u>
<u>L16</u>	l13 not l3	21	<u>L16</u>
<u>L15</u>	l12 not l3	32	<u>L15</u>
<u>L14</u>	l11 not l3	30	<u>L14</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L13</u>	l7 and l10	30	<u>L13</u>
<u>L12</u>	l7 and l9	49	<u>L12</u>
<u>L11</u>	l7 and l8	61	<u>L11</u>
<u>L10</u>	(711/147-173)![CCLS]	12178	<u>L10</u>
<u>L9</u>	(711/111-221)![CCLS]	22657	<u>L9</u>
<u>L8</u>	(712/2-300)[CCLS]	11280	<u>L8</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L7</u>	L1 and address\$5 near8 register\$1 near8 (portion\$1 or file\$1 or sub\$7 or group\$4 or window\$1 or block\$1 or spce\$3 or rang\$3 or area\$1 or region\$1 or section\$1 or bank\$1 or segment\$7 or partition\$6 or divid\$5 or division\$3 or divis\$6 or split\$6 or relat\$4 or absolut\$5) near45 (virtual or relat\$4 or absolut\$5 or physical\$4 or real or linear) near5 address\$6	172	<u>L7</u>
<u>L6</u>	L5 and (virtual or relat\$4 or absolut\$5 or physical\$4 or real or linear) near5 address\$6	380	<u>L6</u>
<u>L5</u>	L1 and address\$5 near8 register\$1 near8 (portion\$1 or file\$1 or sub\$7 or group\$4 or window\$1 or block\$1 or spce\$3 or rang\$3 or area\$1 or region\$1 or section\$1 or bank\$1 or segment\$7 or partition\$6 or divid\$5 or division\$3 or divis\$6 or split\$6 or relat\$4 or absolut\$5)	653	<u>L5</u>
<u>L4</u>	L3 not l2	40	<u>L4</u>
<u>L3</u>	L1 and address\$5 near8 register\$1 near8 (file\$1 or group\$4 or window\$1 or block\$1 or spce\$3 or rang\$3 or area\$1 or region\$1 or section\$1 or bank\$1 or segment\$7) near45 (partition\$6 or divid\$5 or division\$3 or divis\$6 or split\$6 or relat\$4 or absolut\$5)	80	<u>L3</u>
<u>L2</u>	L1 and address\$5 near5 register\$1 near5 (file\$1 or group\$4 or window\$1 or block\$1 or spce\$3 or rang\$3) near35 (partition\$6 or divid\$5 or division\$3 or divis\$6 or split\$6 or relat\$4 or absolut\$5)	40	<u>L2</u>
<u>L1</u>	thread\$3 near5 (multi\$7 or plur\$7 or second or two)	198855	<u>L1</u>

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» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

**1. The compiler for supporting multithreading in cyclic register windows**

Li Cheng; Wang Dingxing; Shen Meiming; Zheng Weimin; Peng Shanling;
 Parallel Architectures, Algorithms, and Networks, 1996. Proceedings. Second International Sympos
 12-14 June 1996 Page(s):57 - 62
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